20 BILLION mobile devices & 100 BILLION intelligent "things"

**Unprecedented volumes of data** will push today’s systems beyond computational limits.

**MEMORY FABRIC SWITCH**
Enables processors to access Fabric-Attached Memory across any node on the system.

**PHOTONICS INTERCONNECTS**
Rapidly transfers data between enclosures with light instead of electricity to access shared memory.

**TASK-SPECIFIC PROCESSING**
Flexible Memory-Driven Computing architecture can match compute tasks to different types of processor to optimize performance and efficiency.

**MEMORY AT THE CENTER**
Combines memory and storage into a vast pool of Fabric-Attached Memory to radically increase computing efficiency and speed by enabling multiple processors to share memory.

**MEMORY-DRIVEN COMPUTING**
Today’s computers chop data to match the limitations of the processor. Memory-Driven Computing puts memory, not the processor, at the core so that technology can overcome the limitations of the processor and enable advancements never before imaginable.

**FAST & POWERFUL**
Simulations based on the Memory-Driven Computing architecture have dramatically increased execution speeds on a variety of workloads.

**SECURE & SMART**
Security is built-in from the microchips up through the fabric, protecting data at rest, in motion and in use.

**OPEN & SIMPLE**
HPE shared development tools for the Memory-Driven Computing ecosystem with the open source community to radically simplify programming and enable new applications.